

PN	Qualified AEC-Q100		Lockstep	Core	Core Frequency	Code Flash/Bank	RAM/TCM	Data Flash	Digital I/Os	Ethernet	OSPI	CAN	CAN FD	UART/LIN	SPI	I²C	CMP	ADC(SAR)	Sigma-delta ADC	FTU	SENT	MSC	HSM	Safety	Package	Pin Counts	Ambient Temperature	Gross Weight (g/pcs)	Tray Packing MOQ(pcs)	Tape&Reel Packing MOQ (pcs)
	Part 1	Part 2																												
FC4150F2MBS1P176T1A	in progress	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	159	100Base-T1(MII/RMII)	x1	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP-EP	176	-40 to 125°C	1.86	n * 400	N/A	
FC4150F2MBS2P176T1A	in progress	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	159	100Base-T1(MII/RMII)	x1	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP-EP	176	-40 to 125°C	1.86	n * 400	N/A	
FC4150F2MBS3P176T1A	in progress	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	159	No	No	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP-EP	176	-40 to 125°C	1.86	n * 400	N/A	
FC4150F2MBS4P176T1A	in progress	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	159	No	No	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP-EP	176	-40 to 125°C	1.86	n * 400	N/A	
FC4150F2MBS1P144T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	123	100Base-T1(MII/RMII)	x1	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F2MBS2P144T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	123	100Base-T1(MII/RMII)	x1	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F2MBS3P144T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	123	No	No	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F2MBS4P144T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	123	No	No	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F2MBS1P100T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	84	100Base-T1(MII/RMII)	x1	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F2MBS2P100T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	84	100Base-T1(MII/RMII)	x1	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F2MBS3P100T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	84	No	No	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F2MBS4P100T1A	Yes	N	ARM Cortex-M4	150MHz	2 MB	256 KB	256 KB	84	No	No	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F1MBS1P144T1A	Yes	N	ARM Cortex-M4	150MHz	1 MB	256 KB	256 KB	123	No	x1	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F1MBS2P144T1A	Yes	N	ARM Cortex-M4	150MHz	1 MB	256 KB	256 KB	123	No	x1	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F1MBS1P100T1A	Yes	N	ARM Cortex-M4	150MHz	1 MB	256 KB	256 KB	84	No	x1	x6	x3	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F1MBS2P100T1A	Yes	N	ARM Cortex-M4	150MHz	1 MB	256 KB	256 KB	84	No	x1	x6	No	x6	x4	x2	x3	x2	N	x8	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F512BS1P144T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	123	No	No	x6	x3	x6	x4	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F512BS2P144T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	123	No	No	x6	No	x6	x4	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F512BS1P100T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	84	No	No	x6	x3	x6	x4	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.67	n * 900	n * 1000	
FC4150F512BS2P100T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	84	No	No	x6	No	x6	x4	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	100	-40 to 125°C	0.67	n * 900	n * 1000	
FC4150F512BS1P64T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	56	No	No	x4	x3	x5	x3	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	64	-40 to 125°C	0.35	n * 1600	n * 1500	
FC4150F512BS2P64T1A	Yes	N	ARM Cortex-M4	150MHz	512 KB/2	128 KB	Configurable	56	No	No	x4	No	x5	x3	x2	x3	x2	N	x6	N	N	HSM	ASIL-B	LQFP	64	-40 to 125°C	0.35	n * 1600	n * 1500	
FC4150F1MBS1P144T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	123	No	x1	x3	x3	x6	x4	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F1MBS2P144T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	123	No	x1	x3	No	x6	x4	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	144	-40 to 125°C	1.33	n * 600	N/A	
FC4150F1MBS1P100T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	84	No	x1	x3	x3	x6	x4	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F1MBS2P100T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	84	No	x1	x3	No	x6	x4	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	100	-40 to 125°C	0.68	n * 900	n * 1000	
FC4150F1MBS1P64T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	56	No	x1	x3	x3	x5	x3	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	64	-40 to 125°C	0.36	n * 1600	n * 1500	
FC4150F1MBS2P64T1B	Yes	N	ARM Cortex-M4	150MHz	1 MB/2	128 KB	128 KB	56	No	x1	x3	No	x5	x3	x2	x1	x2	N	x6	N	N	HSM lite	ASIL-B	LQFP	64	-40 to 125°C	0.36	n * 1600	n * 1500	
FC7300F8MDT2A320T1B	Yes	2 Lockstep+1	ARM Cortex-M7	300MHz	8MB/ 4	1088KB/ 3*192KB	256KB	248	1Gbit with TSN	x1	x10	x10	x18	x8	x2	x3	x4	Not supported, only 4x Digital Filter	12xFTU, x96 ch	2*4	x2	EVITA FULL	ASIL-D	LFBGA	320	-40 to 125°C	0.69	n * 840	N/A	
FC7300F8MDT2P176T1B	in progress	2 Lockstep+1	ARM Cortex-M7	300MHz	8MB/ 4	1088KB/ 3*192KB	256KB	147	1Gbit with TSN	x1	x10	x10	x17	x6	x2	x3	x4	Not supported, only 4x Digital Filter	11xFTU, x88 ch	2*4	x2	EVITA FULL	ASIL-D	LQFP-EP	176	-40 to 125°C	1.91	n * 400	N/A	
FC7300F4MDD3A320T1B	Yes	2 Lockstep	ARM Cortex-M7	300MHz	4MB/ 2	640KB/ 2*192KB	256KB	248	100Mbit with TSN	-	x8	x8	x8	x6	x2	x1	x4	Not supported, only 2x Digital Filter	8xFTU, x64 ch	2*4	x1	EVITA FULL	ASIL-D	LFBGA	320	-40 to 125°C	0.69	n * 840	N/A	
FC7300F4MDD1P176T1B	in progress	2 Lockstep	ARM Cortex-M7	300MHz	4MB/ 2	640KB/ 2*192KB	256KB	147	100Mbit with TSN	-	x8	x8	x8	x5	x2	x1	x4	Not supported, only 2x Digital Filter	8xFTU, x64 ch	2*4	x1	EVITA FULL	ASIL-D	LQFP-EP	176	-40 to 125°C	1.91	n * 400	N/A	
FC7300F4MDS2A320T1B	Yes	1 Lockstep	ARM Cortex-M7	300MHz	4MB/ 2	512KB/ 192KB	256KB	191	-	-	x6	x6	x8	x6	x2	x1	x3	-	6xFTU, x48 ch	2*4	x1	EVITA FULL	ASIL-D	LFBGA	320	-40 to 125°C	0.69	n * 840	N/A	
FC7300F4MDS2P176T1B	in progress	1 Lockstep	ARM Cortex-M7	300MHz	4MB/ 2	512KB/ 192KB	256KB	147	-	-	x6	x6	x8	x5	x2	x1	x3	-	6xFTU, x48 ch	2*4	x1	EVITA FULL	ASIL-D	LQFP-EP	176	-40 to 125°C	1.91	n * 400	N/A	
FC7240F2MDS1P176T1A	in progress	1 Lockstep	ARM Cortex-M7	240MHz	2MB/ 2	256KB/ 160KB	128 KB	153	-	-	x4	x4	x8	x6	x2	x2	x2	-	8xFTU, x64 ch	1*4	x1	EVITA FULL	ASIL-D	LQFP-EP	176	-40 to 125°C	TBD	n * 400	N/A	
FC7240F2MDS1P144T1A	in progress	1 Lockstep	ARM Cortex-M7	240MHz	2MB/ 2	256KB/ 160KB	128 KB	124	-	-	x4	x4	x8	x6	x2	x2	x2	-	8xFTU, x64 ch	1*4	x1	EVITA FULL	ASIL-D	LQFP-EP	144	-40 to 125°C	TBD	TBD	N/A	
FC7240F2MDS1P100T1A	in progress	1 Lockstep	ARM Cortex-M7	240MHz	2MB/ 2	256KB/ 160KB	128 KB	82	-	-	x4	x4	x8	x6	x2	x2	x2	-	8xFTU, x64 ch	1*4	x1	EVITA FULL	ASIL-D	LQFP-EP	100	-40 to 125°C	TBD	TBD	N/A	